

09/071978


$$\begin{array}{r} 714 \\ \hline 812 \end{array}$$

Class	Subclass
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PATENT NUMBER

U.S. REISSUE Patent Application

O.I.P.E.

O.G. PUBLICATION DATE

REISSUE PATENT DATE

FA
SCANNED AG. 3 AB

8-14.01

APPLICATION NO. 09/871978	CONT/PRIOR D	CLASS 714	SUBCLASS 718	ART UNIT 2133	EXAMINER TON 73667
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TITLE . . . APPLICANTS

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Shigeki Tomishima
Tadato Yamagata

Semiconductor integrated circuit comprising synchronous DRAM core and logic circuit integrated into a single chip and method of testing the synchronous DRAM core

PTD-2040
1289

☒ SURRENDER OF ORIGINAL PATENT DT (Ex. Initials) ORIGINAL PATENT NUMBER 5,910,181

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. <u>15</u> Figs. Drwg. <u>15</u> Print Fig. <u>5</u>		CLAIMS ALLOWED Total Claims <u>14</u> Print Claim for O.G. <u>10</u>	
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	DAVID TON PRIMARY EXAMINER <i>David Ton</i> <u>12/18/04</u> (Primary Examiner) (Date)		ISSUE FEE Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	FINAL SPRE REVIEW <u>UNT</u> <u>9/16/05</u> (DETAILS)		ISSUE BATCH NUMBER	


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Issue Classification 	Application No.	Applicant(s)	
	09/871,978	HATAKENAKA ET AL.	
	Examiner	Art Unit	
	David Ton	2133	

ISSUE CLASSIFICATION									
ORIGINAL					CROSS REFERENCE(S)				
CLASS	SUBCLASS				CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
714	718								
INTERNATIONAL CLASSIFICATION									
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(Assistant Examiner) (Date)		DAVID TON PRIMARY EXAMINER		Total Claims Allowed: 14	
<i>W. Jones</i> 2-26-05 (Legal Instruments Examiner) (Date)		<i>David Ton</i> 12/8/04 (Primary Examiner) (Date)		O.G. Print Claim(s) 10	O.G. Print Fig. 5

<input type="checkbox"/> Claims renumbered in the same order as presented by applicant				<input type="checkbox"/> CPA				<input type="checkbox"/> T.D.				<input type="checkbox"/> R.1.47			
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